

Prof.-Dr. Ing. Dietmar Fey

Prof. Dr.-Ing. Dietmar Fey is a Professor of computer science at Friedrich-Alexander-University Erlangen-Nürnberg (FAU) where he leads the Chair for Computer Architecture since 2009. His research interests are in parallel computer architectures, parallel programming environments, embedded systems, processor virtualization, smart sensors and memristive computing. He was involved in several national and international research projects and initiatives on parallel and embedded computing. He participated in the nation-wide priority Program “SPP 1188 - Organic Computing” funded by



German Research Foundation (Deutsche Forschungsgemeinschaft - DFG) and was one of the PIs in the DFG-funded Ph.D. student elite Research Training Group on “Heterogeneous Image Systems”. Furthermore he was involved in the project “Inter-chip-Optical Communications and Photonic PCBs for next generation OBP” funded by European Space Agency (ESA) and in joint projects with industrial partners on Grid and Cloud Computing technology, e.g. OptinumGrid, part of the German D-Grid, funded by German Federal Ministry of Research and Education (BMBF) and Cloud4Eng, part of the nation-wide program Trusted Cloud funded by the German Federal Ministry for Economic Affairs and Energy - BMWi. He has published over 140 articles including 3 books, and about 30 papers in journals. He is a member of HiPEAC (European Network of Excellence on High Performance and Embedded Architecture and Compilation) and of contributors for the new HiPEAC roadmap. Currently he research is on a large joint project with academic partners and industry from Bavarian automotive and avionic industry in the project “Multi-Core safe and software-intensive Systems Improvement Community – FORMUS³IC” funded by Bavarian Research Foundation. His Chair is awarded as an Academic Centers of Excellence of the Heterogeneous Systems Architecture foundation (HSAF) and a member of the EU ICT Cost Action 1401 - Memristors - Devices, Models, Circuits, Systems and Applications (MemoCIS).

Contributions for IARIA:

Keynote Speaker at FUTRURE COMPUTING 2015:

More than the Machine – Using Memristors for Computing

Keynote Speaker at SIGNAL 2016:

Image Processing Applications for Heterogeneous Computing Architectures

Panel Session Chair at FUTURE COMPUTING 2012:

Emerging Computing Paradigms and Their Theoretical and Practical Support Tools.

Proceedings Editor for FUTURE COMPUTING 2012

(together with Lloret Mauri, Jaime)

Further Regular Papers and Talks for FUTURE COMPUTING 2012, VALID 2012, FUTURE COMPUTING 2015, ICONS 2015, SIGNAL 2016.