# **Cyber Threat Response System Design and Test Environment**

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# **Research Necessity and Overview**

01. Necessity of Research02. Scope of Research03. Contents of Research

# Development of core technologies for detecting and responding to cyber threats based on intelligent information technology

 Development of technologies for detecting and responding to cyber threats in nuclear power plants

• Development of AI detection algorithms and simulation-based attack packet generation technologies for Nuclear power plants

 Design of a cyber threat response system for nuclear power plants

# 01. Necessity of Research

□ Internal-External Risk factors in operating a NPPs (Nuclear Power Plants)

• External Risks Factors: (1) Earthquake, (2) Typhoon(strong wind, heavy rain), (3) Aircraft collision, (4)

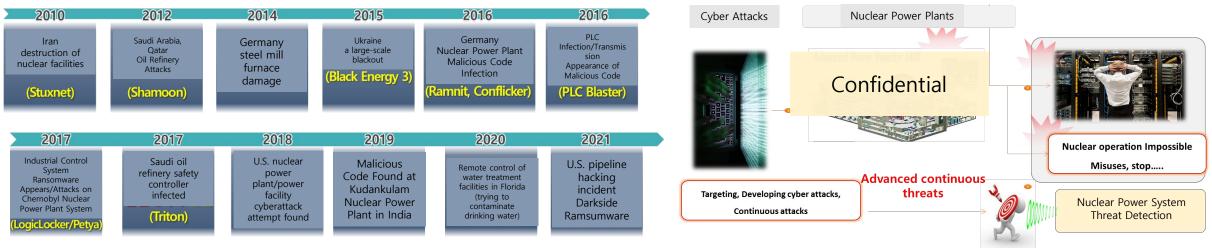
Tsunami(earthquake, storm) (5) Cyber threats

Risk Facors		Limitations in Response	Issues if Unresolved				
	Earthquake	Domestic seismic characteristics and structural deterioration not considered	Loss of core equipment, emergency power supply system, and reactor building function due to an earthquake exceeding design basis				
	Typhoon/Tsun ami	Stress tests not considering domestic characteristics	Loss of heat removal source on primary/secondary side due to external disasters exceeding designables basis (tsunami, storm surge, etc.)				
External	Aircraft	Vibration and internal fire potential due to aircraft collision not considered	Damage to internal system equipment due to vibration, internal fire caused by leaked aviation fuel upon collision				
	Multiple Units	Accident management focused on single unit	Simultaneous core damage and progression to severe accident for multiple units on site				
	Cyber Threats	Lack of cybersecurity technology for nuclear power plant systems	Operational errors, power reduction, unexpected shutdowns, abnormal states, loss of safety system functions				
Internal -	Long-term Operation	Inadequate monitoring and performance degradation assessment system for aging core equipment/components, lack of accident analysis technology considering material characteristics	Potential radioactive material leakage and progression to design basis accident due to core equipment damage, increased likelihood of fuel assembly damage and radioactive material leakage due to loss of internal structural component support capability				
	Human Error	Despite preparation of procedures and training, human error accounts for about 47% of all accidents/failures	Increased likelihood of unexpected shutdowns due to operator judgment/response errors in normal/abnormal operation procedures, potential progression to accident state/severe accident				
	Fire Protection	Conservative evaluation using US fire characteristics information.	Core damage and severe accident due to failure of fire safety shutdown				

# **01. Necessity of Research**

Increasing cyber threats to major infrastructure including domestic and international a NPP.
 New types of threats due to the advancement of cyber threat technologies.

- Cyber threats targeting Industrial Control Systems (ICS) are highly sophisticated and intelligent, often prepared over several years (APT, Advanced Persistent Threats).
- Intelligent and complex cyber-attacks such as manipulation of sensor signals/control logic and modification of HMI (Human Machine Interface) information.
- Difficult to detect intelligent cyber-attacks on ICS with only IT security technologies → necessitating specialized detection technologies for nuclear power plant systems.



<Cyber threat of major infrastructure such as nuclear facilities >

<Cyber Threat Detection Function Need to Nuclear Facility>

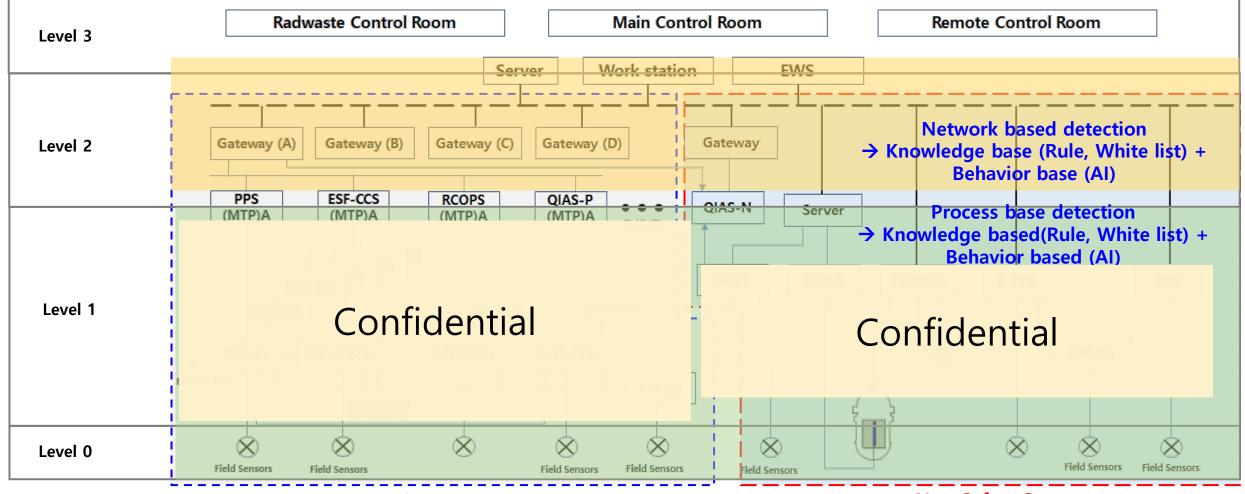
# 01. Necessity of Research

#### Technology for detecting Level 0~2 cyber threats is under development



## 02. Scope of Research

- □ Research on Power Plant Information : Design and Operation Information-Based Detection Technology Research (Focusing on Safety System Process Information)
- Research on the Applicability of Innovative Technologies : Deriving new technology utilization plans through the application of AI technology



Non-Safety System

# 02. Scope of Research

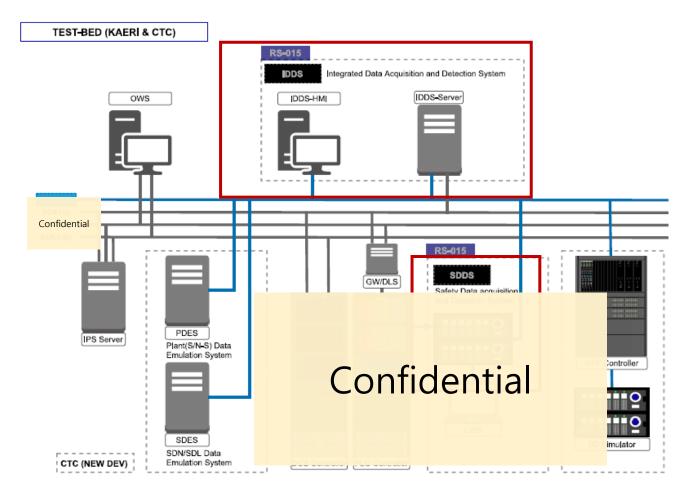
#### Detection System Configuration

• IDDS

- IPS Network Threat Detection in Level2
- IPS Network AI based Threat detection in Level2
- Non-safety Logic based threat detection
- Process signal AI based threat detection

• SDDS

- Safety System Network threat detection in Level1
- Safety System Logic based threat detection
- Implement Detection System
  - SDDS/IDDS Hardware Implementation
  - Level 1/2 Data Interfacing Implementation
  - Data Emulation
- □ Detection system Testing
  - Threat Scenario development and threat data generation

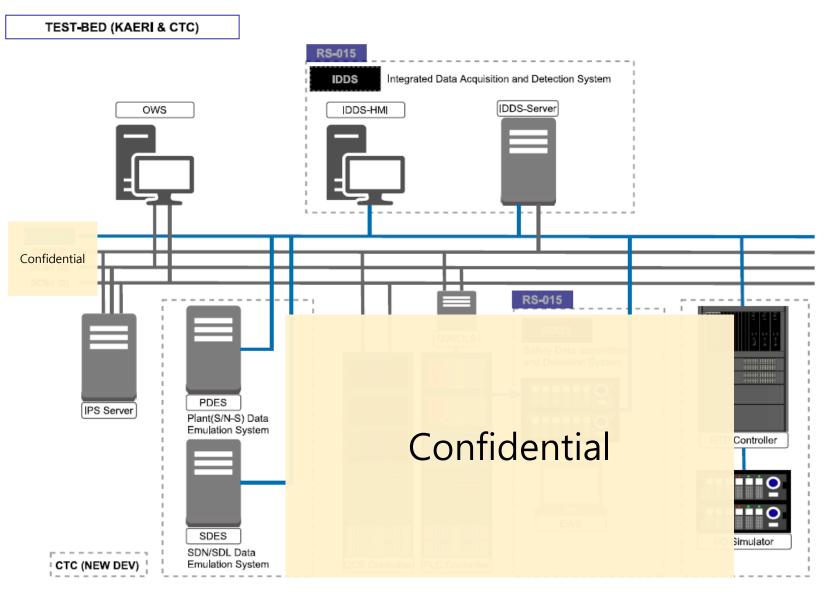


<KAERI TEST-BED based Detection system configuration>

GOAL	Development of design requirements for a cyber threat response system and construction of related data Provision of a development environment for the detection engine for cyber attacks targeting NPP							
	Development of design requirements for a cyber threat response system	Design and development of on-site applications	Construction of normal/abnormal big data	Development of big data collection/processing interface technology				
Activity	<ul> <li>Development of design requirements for test facilities to build a cybersecurity environment</li> <li>Development of a checklist for security testing of the cyber threat response system</li> <li>Design verification based on RS-015 security requirements</li> </ul>	<ul> <li>Design and development of on-site applications for building non-safety system data</li> <li>Design and development of on-site applications for building safety system data</li> <li>Design and development of the HMI (Human-Machine Interface) for the cyber threat response system</li> </ul>	<ul> <li>Data acquisition through a test-bed</li> <li>Generation of simulated data for non-safety systems</li> <li>Generation of simulated data for safety systems</li> <li>Creation of attack simulation data sets</li> </ul>	<ul> <li>Design and production of hardware prototypes for data construction</li> <li>Design and development of communication network interface firmware</li> <li>Design and development of interfaces for connecting safety system servers</li> </ul>				

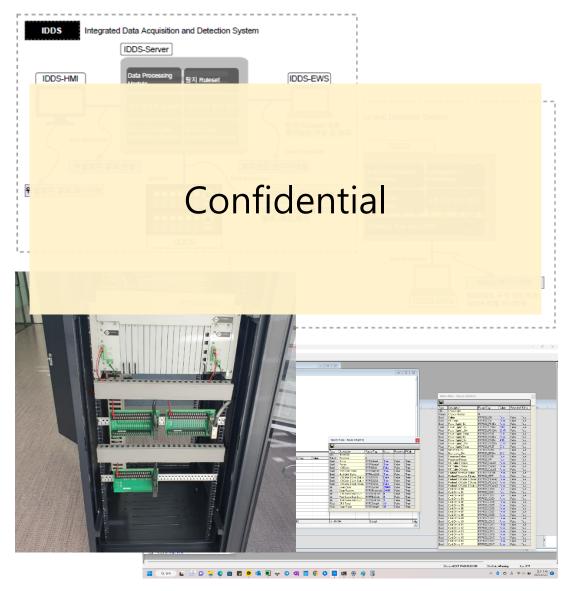
#### □ System Configuration

- **IDDS**(Integrated Data Acquisition and Detection System)
- SDDS(Safety Data acquisition and Detection System)
- **PDES** (Plant (S/N-S) Data Emulation System)
  - Safety/Non-Safety Emulation
     S/W
- SDES (Data Emulation System)
  - Safety Data Emulation S/W



#### Design and development of on-site applications for building non-safety system data

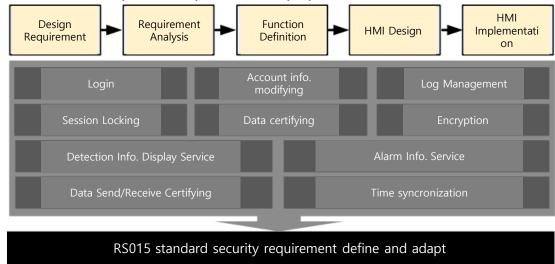
- Design and development of IDDS HMI and interface modules
  - Design for data interface and display of detection information
- Design and development of the IDDS SERVER
  - Preparation of requirements and design documents based on RS-015 security requirements
  - Design and development of MDB, ALARM, and HDSR functions
- Design and development of SDDS / IDDS EWS
  - Preparation of requirements and design documents based on RS-015 security requirements
  - Design and development of engineering tools for SDDS and network-based detection engines
- □ Design and production of non-safety systems based on RTP
  - Design and development of I/O Simulator software

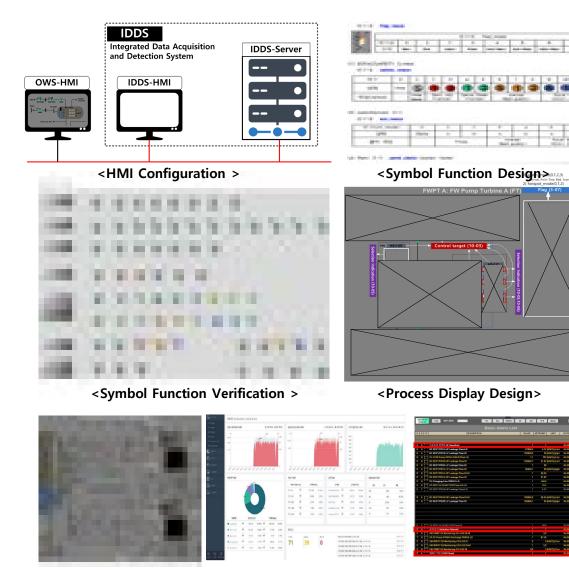


- Design and development of the HMI (Human-Machine Interface) for the cyber threat response system
  - Implementation of the cyber threat detection HMI
    - Design, implementation, and verification of symbol functionality/process

#### screens

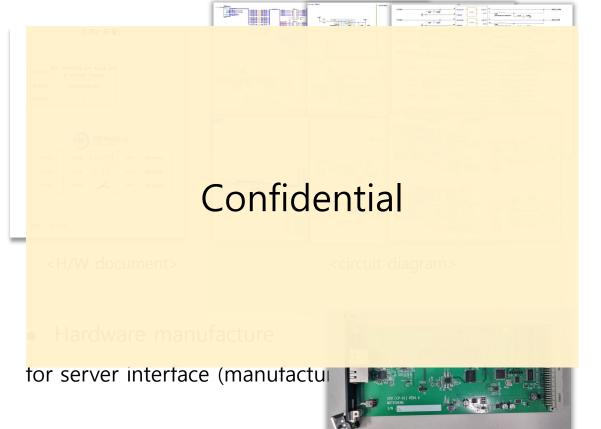
- Design of display screens for network-based detection results
- Design of display screens for process-based detection results
- HMI design for implementing cyber detection screens



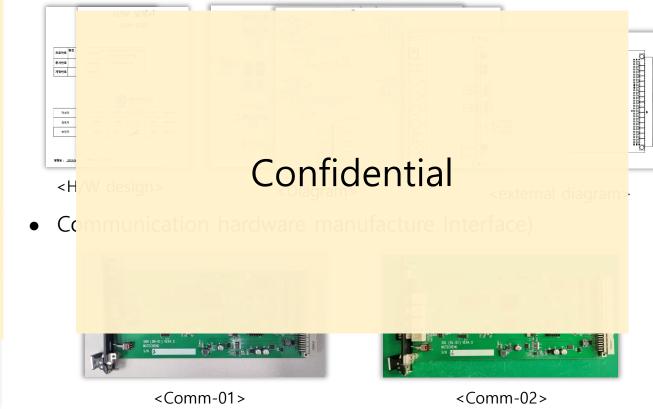


<Process Display Verification> <Network, Detection Display> <Detection Alarm Display>

- Interface Design and Development for safety system server connection.
  - Hardware Design for server connection

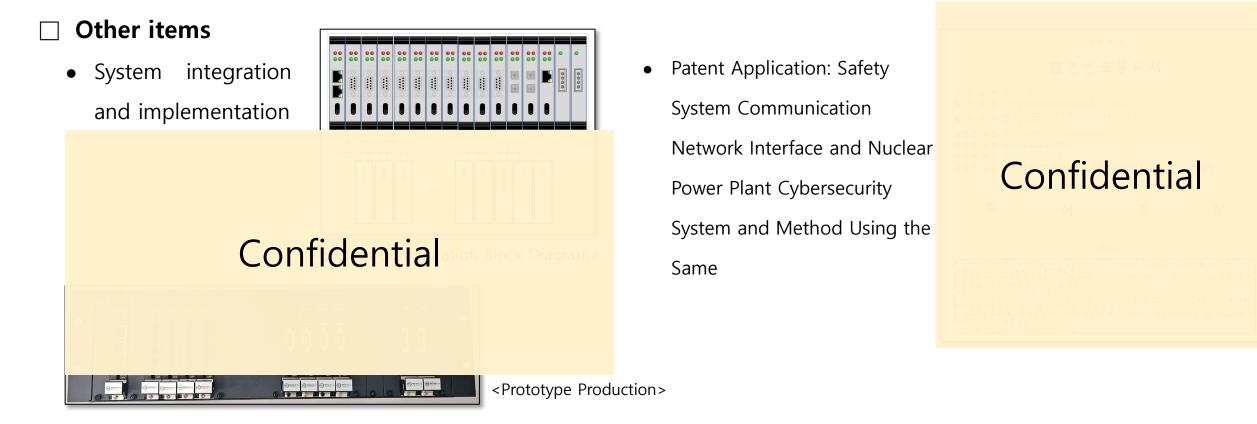


- Hardware prototype design and manufacture for safety system data implementation
  - Interface hardware design for safety system data implementation



Design and development of communication network interface system firmware for building a safety system data

• System firmware development (kernel/device driver: boot config Device Driver, and 17 other items, Firmware: Configuration Module and 6 other types)

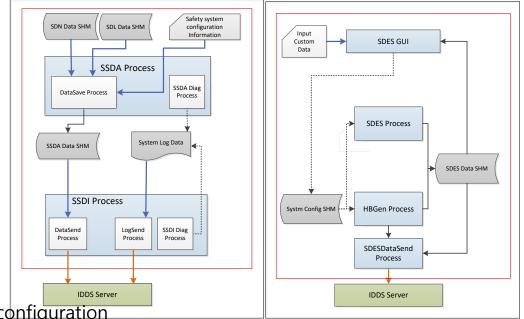


#### **Cyber Threat Response System Design and Implementation (Safety System)**

- Collect/Process and Interface SW detailed design and implementation for Safety System Data Construction
- SSDA Process , SSDI Process Module Configuration
- SDDS receive SDN/SDL Data collection
- Data collect and interface process diagnosis log collection
- Data and diagnosis log data encryption send
- Shared memory data architecture and comm. Protocol definition

#### • Safety Data Emulation SW detailed design and development

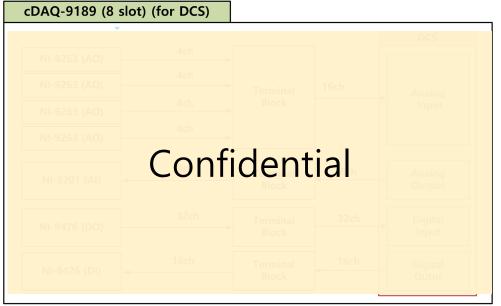
- SDES GUI, SDES Process, HBGen Process, SDESDataSend Process modules configuration
- Controller SDN/SDL input signal and output signal emulation / HeartBeat signal emulation
- Emulation data monitoring
- Emulation data encryption sending
- Shared memory data architecture and communication protocol definition

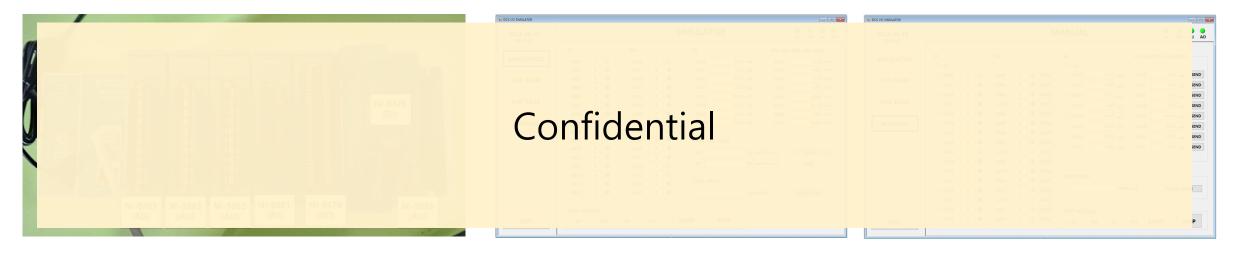


- I/O Hardware implementation and SW development for safety s

   cDAQ-9189 (

   signal emulation
  - Hardware implementation
    - DI(16 channels), DO(32 channels), AI(8 channels), AO(16 channels)
  - SW development
    - Safety system auto operation mode using working file loading function
    - System signal manual operation mode for each bit control
    - Testing process file auto save function and trend analysis function





Safety/Non-safety system data emulation SW design for Normal/Abnormal Big Data

- Safety/Non-safety system emulation SW design and development
- Testing environment support and Test-Bed interface installation

## Interface SW design and development for Big data of non-safety system

- API support to generate data with safety/non-safety scenarios
- Cyber threat response system regulation requirement design verification
  - RS-015 security regulation and secure features design verification (Requirement Specification, System Design, Requirement Traceability Matrix

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771 lation System • = ×	101         SF-CCS         CPU,DMG           102         SF-CCS         CPU,DMG           103         SF-CCS         CPU,DMG           104         SF-CCS         CPU,DMG           105         SF-CCS         CPU,DMG           106         SF-CCS         CPU,DMG           107         SF-CCS         CPU,DMG	Reference Clock Error     Reference Clock Error     Ruit There Clock Warring     Serail Communication Disable     pC052 Scottart Memory Error     pC052 Variable Memory Error     pC052 Alexetion Memory Error	13 14 15 16 17 18	195A 772 공항의리는 2 Exable 195A 772 실력의리는 2 Exable 1958 773 공항의리는 2 Exable 1958 773 상력의리는 2 Exable 1954 773 상력의리는 2 Exable 1954 774 상력의리는 2 Exable	PAD-AA-4678-05         BS SEV 1           PAD-AA-4678-05         PS SEV 1	Inicial         2014         168.1.30.120           Inicial         2025         168.1.30.120           Inicial         2024         168.1.30.120           Inicial         2025         168.1.30.120           Inicial         2025         168.1.30.120           Inicial         2024         168.1.30.120           Inicial         2025         168.1.30.120           Inicial         2025         168.1.30.120	P. 0389. PS P. 0355. PS R. 3031. PS R. 3272. PS R. 3293. PS	Description REACTOR DRAIN THIS PRESS. REACTOR DRAIN THIS PRESS. CCV PRO2A MOTOR WINDOWS CCV PRO2A MOTOR WINDOWS CCV PRO2A MOTOR WINDOWS	TEMP	REAL P Sign	4   System: [P-CCS nol Type: ]AL Ha Type: ]AL
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<Requirement RTM based RS-015 security regulation>

#### □ Normal/Abnormal data generation scenario design based

#### **Distributed Control System**

- Cyber attack environment setting of DCS OS
  - Nuclear non-safety system(FWCS) target set and analysis
  - Process and Network data collect environment set
- Cyber attack scenario design of DCS
  - Attack scenario design based DCS OS vulnerability
  - Attack scenario design based DCS operation environment
- Cyber attack make and analysis of
  - Exploit make for cyber attack
  - Process and Network data collect after cyber attack with scenarios

1 Confidential <process and network data</pre> <Nuclear non-safety system collect environment> target set and analysis> msf6 > use auxiliary/scanner/vxworks/wdbrpc\_bootline msf6 auxiliary(scanner/vxworks/wdbrpc\_bootline) > set RHOSTS 10.10.20.4 RHOSTS => 10.10.20.4 msf6 auxiliary(scanner/vxworks/wdbrpc\_bootline) > run [\*] 10.10.20.4 Error: code=5 Device failed to parse the probe [\*] Scanned 1 of 1 hosts (100% complete) [\*] Auxiliary module execution completed msf6 auxiliary(scanner/vxworks/wdbrpc\_bootline) > set RHOSTS 10.10.20.5 RHOSTS => 10.10.20.5 msf6 auxiliary(scanner/vxworks/wdbrpc\_bootline) > run [+] 10.10.20.5: VxWorks5.5 PC PENTIUM4 host:vxWorks [+] 10.10.20.5: BOOT> lnPci(0,0)host:vxWorks e=10.10.20.5 h=10.10.20.2 u=target2 pw=target2 tn=target2 [\*] Scanned 1 of 1 hosts (100% complete)

[\*] Auxiliary module execution completed

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<Cyber attack action>
(OS vulnerability)
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Co

# □ Normal/Abnormal deriving threat information (malicious code) collecting device and DB design

- OS vulnerability analysis of major system in Test-Bes
  - Nuclear system safety/non-safety OS vulnerability analysis
  - Nuclear system server OS vulnerability analysis

#### • DB design for malicious code data

- Malicious code survey/collect based predefined OS
- Malicious DB architecture design based surveyed malicious code

#### Development Program(SDDS/IDDS) SW function verification

- SW function verification for safety system program
  - Verification methods setting for SDDS SW
  - Verification perform for SDDS SW
- SW function verification for non-safety system program
  - Verification methods setting for IDDS SW
  - Verification perform for IDDS SW

fication							
Traceability	IEEE Std. 1012	Interface Analysis					
Correctness	Correctness	Correctness					
Consistency	Consistency	Consistency					
Completeness	Completeness	Completeness					
	Accuracy	Rightness					
	Readability	Testability					
	Testability						

<SW function verification and validation methodology>

# 

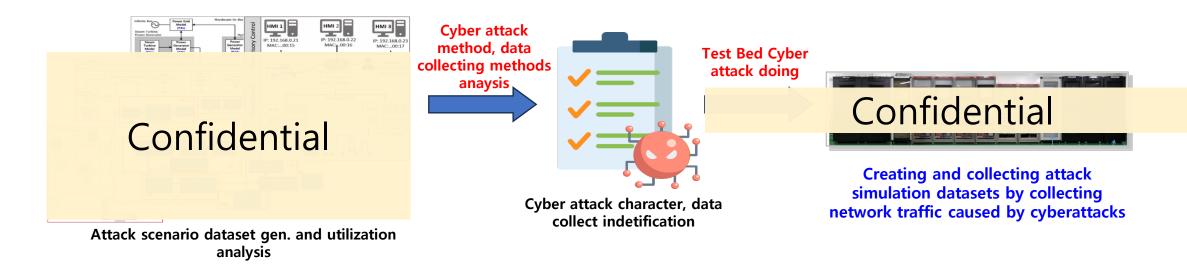
## Development of a checklist for testing the security of syste ms for dealing with cyber threats

- Research and analysis of security testing for intrusion detection syste ms within the domestic information protection product evaluation an d certification system.
- Derivation of security test checklist items applicable to cyber threat reponse systems
  - Providing criteria for items excluded from evaluation and certification system s and security evaluation documents
- Development of a security test checklist by compiling security test items derived from each document
  - Categorizing security test items into areas such as security audit, commu nication, cryptographic support, user data protection, identification and a uthentication, security management, system security functions, resource utilization, system access management, and secure path/channel



#### Development of an attack simulation dataset for testing cyber threat response systems

- Case study on using an attack simulation dataset for performance testing of anomaly detection systems targeting con trol systems
- Development of an attack simulation dataset for detection performance testing of cyber threat response systems
  - Analysis of cyber attack execution methods and data collection strategies for control system testbeds through case analysis of each attack simulation dataset
  - Completion of analysis on six datasets, including the National Security Research Institute's HIL (Hardware-in-the-Loop) based augmented ICS (HAI) testbed dataset



# Thank you.